(Please write your Exam Roll No.) *Exam Roll No. .....*

END TERM EXAMINATION

FIFTH SEMESTER [B.TECH] JANUARY 2024

Paper Code: HS-301 ***Subject: Economics for Engineers***

Time: 3 Hours ***Maximum Marks: 75***

Note: Attempt five questions in all including Q. No.1 which is compulsory. Select one question from each unit.

Q1 Answer the following questions briefly:-
(a) Elaborate resource constraint as an economic problem. **(3)**
(b) Justify how indifference curve helps in understanding consumer's choice. **(3)**
(c) What is oligopoly market. **(3)**
(d) Define NNP, GNP, GDP. **(3)**
(e) State the Law of Demand. **(3)**

UNIT-I

Q2 (a) List the differences between Microeconomics and Macroeconomics. **(5)**
(b) List the factors effecting demand and supply. **(5)**
(c) Explain how Production Possibility Curve helps in product choice decisions. **(5)**

Q3 (a) Explain the circular flow of income in an economy. What does it measure? **(8)**
(b) Explain the elasticity of demand and supply with example. **(7)**

UNIT-II

Q4 (a) How Cardinal and Ordinal utilityare used to analyze consumer behaviour. **(8)**
(b) What is demand forecasting? List the features demand forecasting. How it proves a useful tool for decision makers. **(7)**

Q5 (a) List different methods of demand forecasting. Explain any two quantitative methods of demand forecasting. **(8)**
(b) Explain about consumer equilibrium. **(7)**

UNIT-III

Q6 (a) Define 'Cost'. How are costs classified? Explain any two important cost concepts useful for managerial decisions. **(8)**
(b) Distinguish between monopoly and perfect competition. **(7)**

Q7 (a) Define economics of scales and diseconomies of scales. **(8)**
(b) Differentiate between perfect competition and monopolistic competition? **(7)**

UNIT-IV

Q8 (a) What are the major macroeconomic issues directly related to business decision making? **(8)**
(b) Explain different phases of business cycle? **(7)**

Q9 (a) What is inflation. Briefly explain the features and types of inflation. **(8)**
(b) Explain different approaches used in calculating national income? **(7)**

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END TERM EXAMINATION

FIFTH SEMESTER [B. TECH] JANUARY 2024

Paper Code: CIC-303 **Subject: Compiler Design**

Time: 3 Hours **Maximum Marks: 75**

Note: Attempt five questions including Q. No. 1 which is compulsory. Select one question from each unit. Assume missing data.

Q1. Answer the following questions: (5*3=15)

a) What is Translator? Differentiate between an interpreter and a compiler in terms of functionality.

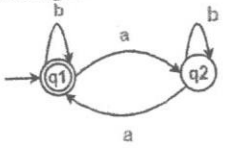
b) Define the term "parser" and its role in the context of compiler design.

c) What is left recursion? Eliminate left recursion from the following grammar:

E -> E + T | T
T -> T * F | F
F -> (E) | id

d) What is DAG? What are the advantages of DAG?

e) Consider the transition diagram



Convert the above finite automata into the regular expression.

UNIT-I

Q2. a) Define the term "compiler" and explain its role in the context of programming languages. Also explain structure and phases of a compiler in detail. (8)

b) How do we implement lexical analyzer? Explain step by step procedure for implementing lexical analyzer for the following LEX program: (7)

```
AUXILLARY DEFINITIONS:
        (none)
TRANSLATION RULES:
  a      {} /* Actions are omitted here*/
  abb    {}
  a*b+   {}
```

Q3. a) Write step by step procedure to construct the NFA for the following regular expression: (8)

R = (a | b)*abb

b) What is input buffering? How it helps in construction of compiler? Explain. (7)

P.T.O.

CIC-303

UNIT-II

Q4. a) Consider the following grammar: (5)

S -> ABC, A-> Aa | d, B-> Bb | e, C-> Cc | f

Eliminate left recursion from the above grammar.

b) Explain and remove the ambiguity from following CFG. (5)

E-> E+E | E-E | E/E | E*E | (E) | -E | id

c) Find the FIRST and FOLLOW for the following given grammar: (5)

E -> TE'
E' -> +TE' | ε
T -> FT'
T' -> *FT' | ε
F -> (E) | id

Q5. a) Test whether the grammar is LL (1) or not and construct a predictive parsing table for it. (8)

S -> iCtSS' | a
S' -> eS | ϵ
C -> b

b) Construct the LR(0) parsing table for the following grammar. (7)

S->L=R
S->R
L->*R
L->id
R->L

Check whether the above grammar is LR (0) grammar or not.

UNIT-III

Q6. a) State and explain the syntax directed translation scheme for the desk calculator and give the parse tree and translation for the string (8)

w = 20 * 7 + 3.

b) What is intermediate code representation? Explain various intermediate code representation schemes with the help of an example. (7)

Q7. a) What do you mean by three address code? Convert the following statements into the Quadruple, Triple and Indirect triple representation: (A+B)*(C-D*E) (8)

b) What is type checking? Explain rules of type checking in context of semantic analysis. (7)

UNIT-IV

Q8. a) What is the use of symbol table? Explain the any two data structures associated with symbol table in detail. (8)

b) Write short note on the following: (7)
   a) Loop unrolling & Loop jamming
   b) Code generation

Q9. a) Write short note on the following: (8)
   a) Basic blocks & flow graph
   b) Peephole optimization

b) Explain the various types of errors generated during the various phases of the compiler. How do we recover from these errors? (7)

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CIC-303

(Please write your Exam Roll No.) *Exam Roll No. .....*

END TERM EXAMINATION

FIFTH SEMESTER (B.TECH) JANUARY-2024

Paper Code: CIC-307 ***Subject: Computer Networks***

Time: 3 Hours **Maximum Marks: 75**

Note: Attempt five questions in all including question no.1 which is compulsory. Select one question from each unit. Assume missing data, if any.

Q1 Attempt **(Any Five)** **(3x5=15)**

a) What is the purpose of a switch in a network?
b) Explain the difference between half-duplex and full-duplex communication.
c) Explain the purpose of a subnet mask in IP networking.
d) Define the role of a DNS server in network communication.
e) Define the term "fiber optic cable" and explain its advantages in data transmission.
f) How flow and error control works in data link layer?

UNIT-I

Q2 a) What is OSI model. Explain its layers in detail with the help of diagram. Also list the difference between OSI and TCP/IP model? **(10)**

b) An 8 bit byte binary value 10101111 is to be encoded using an even parity Hamming code. What is the binary value after encoding? **(5)**

Q3 a) Explain different types of Guided and Unguided transmission media with the help of diagram. Also list out the advantages and disadvantages of each? **(10)**

b) What is Packet Switching? Explain Virtual circuit and Datagram approach in detail? **(5)**

UNIT-II

Q4 a) What are the various design issues in Data Link Layer? Explain the Stop and Wait ARQ protocol with diagram. **(8)**

b) Compare and contrast HDLC and PPP. Explain the frame format of both with a diagram. **(7)**

Q5 a) Compare and contrast the features of repeaters, hubs, switches, and bridges. Describe the functions of each device and emphasize how each one manages traffic flow and improves network performance. **(8)**

b) Explain channel allocation problem and its solution. Provide an example to illustrate. **(7)**

UNIT-III

Q6 a) What is the difference between classful and classless IP addressing? Explain using an example. **(5)**

b) Explain the Leaky bucket algorithm and illustrate how traffic congestion can be reduced. **(5)**

c) What is the maximum number of subnets in each case? **(5)**

i) Class A; mask 255.255.192.0
ii) Class B; mask 255.255.192.0
iii) Class C; mask 255.255.255.192
iv) Class D; mask 255.255.255.240

P.T.O.

CIC-307

Q7 a) What are distance-vector and link-state routing algorithms. Explain? **(5)**

b) Describe the role of ARP in computer networks. How does ARP help devices communicate within the same network? Provide a basic explanation of the ARP process. **(5)**

c) An organisation is granted the block 211.17.180.0/24. The administrator wants to create 32 subnets. **(5)**

i) Find the subnet mask.
ii) Find the number of addresses in each subnet.
iii) Find the first and last addresses in subnet 1.
iv) Find the first and last addresses in subnet 32.

UNIT-IV

Q8 a) Explain the differences between UDP and TCP in the transport layer. Discuss their main characteristics and provide examples where using UDP might be more advantageous than TCP? **(7.5)**

b) Describe the importance of congestion control in the transport layer. How does congestion control contribute to maintaining network stability and ensuring a reliable data transfer process? **(7.5)**

Q9 a) Explain the client-server model in the context of the application layer. Discuss the roles of clients and servers in this model, highlighting how communication is established between them? **(7.5)**

b) Provide an overview of the Domain Name System (DNS). How does DNS work to translate domain names into IP addresses? Explain the role of DNS servers in facilitating web browsing and internet communication? **(7.5)**

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CIC-307

(Please write your Exam Roll No.) *Exam Roll No. .....*

END TERM EXAMINATION

FIFTH SEMESTER [B.TECH] JANUARY 2024

Paper Code: CIC-309 ***Subject: Software Engineering***

Time: 3 Hours ***Maximum Marks: 75***

Note: Attempt five questions in all including Q. No.1 which is compulsory. Select one question from each unit. Use of simple Calculator is allowed.

Q1 Answer **any five** questions:- **(5x3=15)**

(a) Briefly explain the concept of Software Crisis and how it differs from common Software Myths. Provide real-world examples.

(b) Distinguish software re-engineering and reverse engineering?

(c) Calculate and interpret Halstead's effort for a program where the number of distinct operators is n_1=15, the number of distinct operands is n_2=20, the total number of operators isN_1=120, and the total number of operands is N_2=80.

(d) Discuss the implications of high coupling on software maintainability, with examples from industry practices.

(e) Discuss the role of regression testing in Agile methodologies.

(f) Draw the Context level 0 and level 1 DFD for the Safe home Software?

(g) Assess the effectiveness of data dictionaries in managing large-scale software projects, citing real-world applications.

(h) Develop a set of test cases to perform boundary value analysis for the following input fields in an online registration form:

(i) Age: The form requires students to enter their age, which must be between 18 and 30 years inclusive.

(ii) Number of Completed High School Subjects: The form also asks for the number of completed high school subjects, which should be a minimum of 5 and a maximum of 12.

Ensure your test cases cover the boundary conditions for these input fields.

UNIT-I

Q2 (a) Compare and contrast the Waterfall and evolutionary Models of SDLC. Focus on key stages, flexibility, risk management, and suitability for different types of projects. Provide examples. **(7.5)**

(b) Explain the importance and steps of requirement elicitation in software engineering. Discuss the FAST and Use Case approaches, highlighting their strengths and limitations. **(7.5)**

Q3 (a) Describe the key characteristics and structure of an effective Software Requirement Specification (SRS) document. Use IEEE standards as a reference. **(7.5)**

(b) Discuss the differences and similarities between Functional and Non-Functional requirements with examples. How do these impact the software development process? **(7.5)**

P.T.O.

CIC-309
P-1/2

UNIT-II

Q4 (a) Explain the COCOMO cost estimation model. Perform a basic COCOMO calculation for a project with a given size (50 KLOC). Using the Intermediate COCOMO model, calculate the effort in person-months. Assume the project has 'HIGH' complexity [CMPLX=High=1.15]. **(5)**

(b) Describe the concepts of cohesion and coupling in software design. Provide examples of each and discuss their impact on software maintainability and flexibility. **(5)**

(c) Write a short note on live variables and module weakness. **(5)**

Q5 (a) Discuss the Putnam Resource Allocation Model, including its application and limitations. Perform a calculation using given project parameters. **(5)**

(b) Compare and contrast Object-Oriented Design (OOD) and Function-Oriented Design (FOD) in terms of their methodology, advantages, and disadvantages. **(5)**

(c) Differentiate between Function Point and LOC software Metrics. Why Function points metrics are preferred over Lines of Code Metrics. **(5)**

UNIT-III

Q6 (a) Explain Halstead's Software Science metrics. Write equations to calculate the Effort and Time for a given program using Halstead's methos. **(7.5)**

(b) Describe the concept of Software Reliability. Compare the Basic Model and Logarithmic Poisson Model in terms of their approach and applicability. **(7.5)**

Q7 (a) Discuss the importance of software metrics in software engineering. Explain Token Count and Information Flow metrics with examples. **(7.5)**

(b) Explain the CMM (Capability Maturity Model) and ISO 9001 standards. Discuss how they contribute to software quality assurance. Give example of three CMM Level 3 companies based in India. **(7.5)**

UNIT-IV

Q8 (a) Describe the testing process in software engineering, focusing on the importance of functional and structural testing. Provide examples of test cases for each. **(5)**

(b) Create Control Flow Graph for a module that checks if sum of 2 numbers is positive, negative or neutral. Also find cyclomatic complexity number for this module and number of independent paths in the module. **(5)**

(c) Explain the concept and process of Software Maintenance. Discuss the Quick-fix and Boehm's models of maintenance with their merits and demerits. **(5)**

Q9 (a) Define and differentiate between Verification and Validation in software testing. Provide examples to illustrate your points. **(5)**

(b) Discuss the importance and methods of Software Configuration Management in the context of software maintenance. **(5)**

(c) Explain the importance of acceptance testing in software development. Compare and contrast alpha and beta testing. **(5)**

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P-2/2

(Please write your Exam Roll No.) Exam Roll No. .....

END TERM EXAMINATION

FIFTH SEMESTER (B.TECH) JANUARY-2024

Paper Code: CIC-311 **Subject: Design Analysis & Algorithm**

Time: 3 Hours **Maximum Marks: 75**

Note: Attempt five questions including Q. no.1 which is compulsory. Select one question from each unit.

(3×5=15)

Q1 Attempt **Any five** questions:

a. What is an Algorithm and also discussed the characteristics of algorithm.

b. Define big oh(O),Big omega(Ω) and big theta(Θ) notations

c. What is the use of Warshall's algorithm and Floyd's Algorithm?

d. What are NP Hard and NP Complete problems?

e. Explain local search heuristic with example

f Explain optimal binary search tree Problem with example

g Apply the Quick sort to the list E , X , A , M , P , L , E

UNIT I

Q2 a. What do you mean by complexity of an algorithm? Explain the notations used to denote the complexity of an algorithm. **(7)**

b. Explain the following with example: **(8)**

(i) Selection sort.

(ii) Disjoint Sets.

Q3 a. How the operations performed in Strassen's Matrix multiplication **(7)**

b Explain in detail merge sort. Illustrate the algorithm with a numeric example. Provide complete analysis of the same. **(8)**

UNIT II

Q4 a Explain Prim's algorithm for minimal spanning tree with an example. **(7)**

b Write in detail about Hamiltonian cycles. Give example to it. **(8)**

Q5 a Write backtracking algorithm for **(7)**

I. The n-queens problem
II. Hamiltonian problem
III. The subset-Sum problem

b Define Greedy knapsack. Find the optimal solution of the Knapsack instance n= 7, M=20, (p1, p2, ...p7) = (8,5,6,7,6,12,3) and (w1,w2,....w7)=(2,10,8,7,6,4,11) **(8)**

UNIT-III

Q6 a Explain the Travelling salesmen problem using Branch and bound technique.. **(7)**

b What is backtracking? Solve 8 queens problem with backtracking. **(8)**

Q7 a What do you mean by dynamic Programming? Explain O/I knapsack problem by using dynamic programming. **(7)**

b Explain the chained matrix multiplication with suitable example **(8)**

P.T.O.

UNIT-IV

Q8 a Explain the following **(7)**

(a) NP hard Graph.
(b) NP scheduling problems

b What are NP Hard and NP Complete problems? Explain Cook's theorem **(8)**

Q9 a Explain non-deterministic algorithm with an example. **(7)**

b What is state space tree? What are the different ways of searching an answer node in an state space tree explain with example. **(8)**

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(Please write your Exam Roll No.) Exam Roll No. .....

END TERM EXAMINATION

FIFTH SEMESTER [B.TECH] JANUARY 2024

Paper Code: ECC-305 **Subject: Microelectronics**

Time: 3 Hours **Maximum Marks: 75**

Note: Attempt five questions in all including Q. No.1 which is compulsory. Select one question from each unit. Each question carry equal marks. Assume suitable missing data if any.

Q1 (a) Differentiate between enhancement and depletion type MOSFET. Which type is a preferred choice and why? Support your answer with relevant diagrams. **(2.5)**

(b) What is Moore's Law? With downscaling of device dimensions by a factor of 10, what would you expect for drain current will be ---- and --- times of the original value if constant field scaling and constant voltage scaling are used. **(2.5)**

(c) Draw the static CMOS logic circuit for the following expression: **(4)**
(i) Y= (AB'CD)' (ii) Y= [D(A+BC)]'

(d) Write short notes on: **(1x6=6)**
(i) Channel Length Modulation
(ii) Ferroelectric RAM
(iii) Threshold Voltage
(iv) Twin Tub Process
(v) Impact of increasing threshold voltage of NMOS transistor on CMOS VTC in terms of noise margins (VIL, VIH).
(vi) CMOS vs BJT

UNIT-I

Q2 (a) An NMOS transistor, operating in the linear region with VDS = 50 mV, is found to conduct 25 μA for VGS = 1 V and 50 μA for VGS = 1.5 V. What is the apparent value of threshold voltage VT? If kn' = 50 μA/V2, what is the device W/L ratio? What current would you expect to flow with VGS = 2 V and VDS = 0.1 V? If the device is operated at VGS = 2 V, at what value of VDS will the drain end of the MOSFET channel just reach pinch-off? **(7.5)**

(b) Explain in detail a typical N-well CMOS fabrication process. Support it with suitable diagrams. **(7.5)**

Q3 (a) With the help of energy band diagram, explain the operation of MOSFET under external bias. **(7.5)**

(b) Derive the expression for IDS for NMOS transistor in non-saturation region and draw IDS vs VDS characteristics for enhancement mode MOS transistor. **(7.5)**

UNIT-II

Q4 (a) Design a CMOS inverter with switching threshold voltage of 2 /3 *VDD*. What is the resulting ratio of *WP /WN* ? (Assume LP = LN, μN = 2.5μP, |VTP|=VTN = 0.2 VDD). **(2.5)**

(b) Design the following Boolean functions using multiple output domino logic: **(5)**
C1 = G1 + P1C0, C2 = G2 + P2G1+ P2P1C0, where Pi = Ai Oi +AiOi , Gi = Ai Oi (i = 1,2,3)

(c) What is a pass transistor? Explain the different regions of operation of CMOS inverter. Support with a diagram. **(5)**

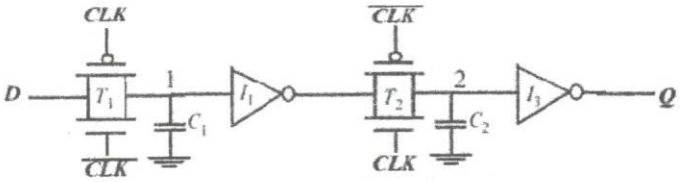
(d) Explain CMOS based NOR SR latch circuit and its working. **(2.5)**

P.T.O.

Q5 (a) Define Latch-up. Is it desirable? Give the ways to reduce the same in CMOS circuits. **(4)**

(b) Discuss dynamic CMOS logic. Consider a three input NOR gate. **(5)**

(c) An implementation of a D Flip-flop is show in Fig. Answer the following: **(6)**
(i) Is this a static or dynamic flip-flop?
(ii) Is this positive edge triggered or negative edge triggered?
(iii) Calculate tsetup, tCtoQ and thold for this flop in terms of the transmission gate and inverter delays.



UNIT-III

Q6 (a) Explain the difference between cascade current mirror and Wilson current mirror with diagrams. **(7.5)**

(b) Explain the compensation of op-amps. **(7.5)**

Q7 (a) Develop CMOS Differential amplifier and obtain the differential Transconductance of the same. **(7.5)**

(b) Derive the expression for power-supply rejection ratio of Two-stage op-amps. **(2.5)**

(c) What is a current mirror circuit? Explain with the help of a circuit. **(5)**

UNIT-IV

Q8 (a) Explain read/write operation of SRAM memory cell. How 1 bit cell is used in bigger memory systems. **(7.5)**

(b) Distinguish between SRAM and DRAM. **(7.5)**

Q9 (a) Implement 2:1 MUX using CMOS transmission gate. **(5)**

(b) Write short notes on:
(i) Analog to Digital Converters (ADCs) **(5)**
(ii) Digital to Analog Converters (DACs) **(5)**

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(Please write your Exam Roll No.) *Exam Roll No. .....*

END TERM EXAMINATION

FIFTH SEMESTER [B.TECH] JANUARY 2024

Paper Code: ECC-309 ***Subject: Transmission Lines, Waveguides and Antenna Design***

Time: 3 Hours ***Maximum Marks: 75***

Note: Attempt five questions in all including Q. No.1 which is compulsory. Select one question from each unit. Assume missing data, if any.

Q1 Attempt all questions:-

(a) Define dominant, degenerate and evanescent modes. **(3)**

(b) Write wave equations for voltage and current for a transmission line. **(3)**

(c) Define loaded and unloaded Q for series and parallel resonance circuits. **(3)**

(d) Define the following with respect to antenna: **(3)**

(i) Major lobe & Minor lobe

(ii) Antenna efficiency & Radiation Efficiency

(iii) Directivity & Gain of an antenna

(e) What is fringing in Microstrip transmission line? How to account for this in the microstrip transmission line? **(3)**

UNIT-I

Q2 (a) Derive impedance for shorted and open circuited transmission line. **(5)**

(b) A telephone line has R=30Ω/km, L=100 mH/km, G=0 and C=20μF/km. At f=1 KHz, obtain: **(5)**

(i) Characteristics impedance of the line

(ii) Propagation Constant

(iii) Phase Velocity

(c) Write short note on the following **(any two)**: **(5)**

(i) Characteristic Impedance

(ii) Voltage standing wave ratio

(iii) Reflection Coefficient

Q3 (a) Explain the design of the Smith Chart. Also explain how to find impedance at the distance 'l' from the load using Smith Chart. **(5)**

(b) A 600Ω lossless transmission line is fed by 50Ω generator. If the line is 200m long terminated by a load of 500Ω. Determine in dB, **(5)**

(i) Reflection Loss

(ii) Transmission Loss

(iii) Return Loss

(c) Write short note on the following **(any two)**: **(5)**

(i) Quarter wave transformer

(ii) Stub matching

(iii) Microstrip Transmission Line

UNIT-II

Q4 (a) Derive field components of TE waves in rectangular waveguide. Also deduce instantaneous field expressions for TE_{01} mode. **(10)**

(b) In a rectangular waveguide for which a=1.5cm, b=0.8cm, σ=0, μ=μ_0 and ϵ=4ϵ_0. **(5)**

$$H_x = 2\sin\left(\frac{\pi x}{a}\right)\cos\left(\frac{3\pi y}{b}\right) sin(\pi X 10^{11} t - \beta z)\, A/m$$

P.T.O.

Determine:
(i) The mode of operation
(ii) The cutoff frequency
(iii) The phase constant
(iv) The propagation constant
(v) The internal wave impedance

Q5 (a) Derive field components of TE waves in circular waveguide. Show dominant mode in circular waveguide. **(10)**

(b) An air-filled circular waveguide having an inner radius of 0.8cm is excited in dominant mode at 10 GHz. Find the cut off frequency of dominant mode, guide wavelength and bandwidth for operating in dominant mode. **(5)**

UNIT-III

Q6 (a) Derive an expression for unloaded Q of a transmission line resonator consisting of a short-circuited transmission line $\lambda/2$ long. **(5)**

(b) A circular cavity resonator with d=2a is to be designed to resonate at 5.0 GHz in the TE_{011} mode. If the cavity is made from copper and is Teflon filled (ϵ_r=2.1, tanδ=0.0004), find its dimension and unloaded Q. **(5)**

(c) Compute the resonant frequency of a cylindrical resonator with ϵ_r=10.8, 2a=11.71mm and L=2.14mm. **(5)**

Q7 (a) Derive the unloaded Q for the TM_{111} mode of a rectangular cavity, assuming lossy conducting walls and lossless dielectric. **(5)**

(b) A rectangular cavity resonator is constructed from 3.0 cm length of aluminum X-band waveguide. The cavity is air filled. Find the resonant frequency and unloaded Q of the TE_{101} and TE_{102} resonant modes. **(5)**

(c) Explain in detail development of gap-coupled microstrip resonator. **(5)**

UNIT-IV

Q8 (a) Define and derive power spectral density of an antenna. Establish relation between radiation intensity and total power radiated by an antenna. **(7)**

(b) Derive Friis transmission equation for transmitting and receiving antennas. **(5)**

(c) An antenna has a normalized radiation intensity given by $U(\theta)=\cos^2(3\theta)$ for $0\leq\theta\leq\Pi/2$ and $0\leq\varphi\leq2\Pi$. Find HPBW and FNBW in degrees. **(3)**

Q9 (a) Obtain the expression for Electric Field Intensity and Magnetic Field Intensity due to magnetic vector potential A for electric current source J only.Write expressions for $\vec{E}$ and $\vec{H}$ for Magnetic current source $\vec{M}$ using duality theorem. **(10)**

(b) State Reciprocity theorem for antennas in differential and integral form. **(5)**

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(Please write your Exam Roll No.) *Exam Roll No. .....*

END TERM EXAMINATION

FIFTH SEMESTER (B.TECH) JANUARY-2024

Paper Code: ECC-311 ***Subject: Data Communication & Networking***

Time: 3 Hours ***Maximum Marks :75***

Note: Attempt five questions in all including Q. no.1 which is compulsory. Select one question from each unit.

Q1	(a)	What are the responsibilities of Transport Layer? Explain	(2.5)
	(b)	What are the differences between Router and Switch?	(2.5)
	(c)	What do you understand by tunnelling?	(2.5)
	(d)	Explain the differences between Adaptive and NON Adaptive Routing.	(2.5)
	(e)	Explain the design issues of Data Link Layer	(2.5)
	(f)	Define the following terms: (i) Full Duplex, (ii) Burst Error, (iii) Firewall	(2.5)

UNIT- I

Q2	(a)	What are the differences between TCP/IP and OSI model	(7)
	(b)	Write short note on network topologies of WAN.	(8)
Q3	(a)	Explain the importance and functionality of guided media in detail. Explain the types of guided media in detail.	(7)
	(b)	Explain Circuit Switching and Packet Switching in detail with example for each specific type? Provide diagrams for the two types of Packet Switching that is (a) datagram and (b) virtual circuit switching.	(8)

UNIT- II

Q4	(a)	Discuss Stop and Wait ARQ error control in data link layer	(7)
	(b)	Explain in detail the design issues of Data Link layer.	(8)
Q5	(a)	Compare and contrast the Go-Back-N ARQ protocol with Selective repeat ARQ.	(7)
	(b)	Explain the error detection techniques. Encode a binary word 11001 into the even parity hamming code.	(8)

UNIT- III

Q6	(a)	Differentiate between IPv4and IPv6	(7)
	(b)	Explain Shortest path Routing Algorithm with suitable example	(8)
Q7	(a)	What are the role of IP address ? Explain its different classes	(7)
	(b)	Explain Leaky Bucket Algorithm with suitable example	(8)

UNIT- IV

Q8	(a)	What is ATM. Give its architecture?	(7)
	(b)	Discuss about congestion control and traffic management.	(8)
Q9	(a)	Write a short note on Dynamic Host Configuration Protocol (DHCP)	(7)
	(b)	Write a short note on Hypertext Transfer Protocol	(8)

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ECC-311